

fig. 1A

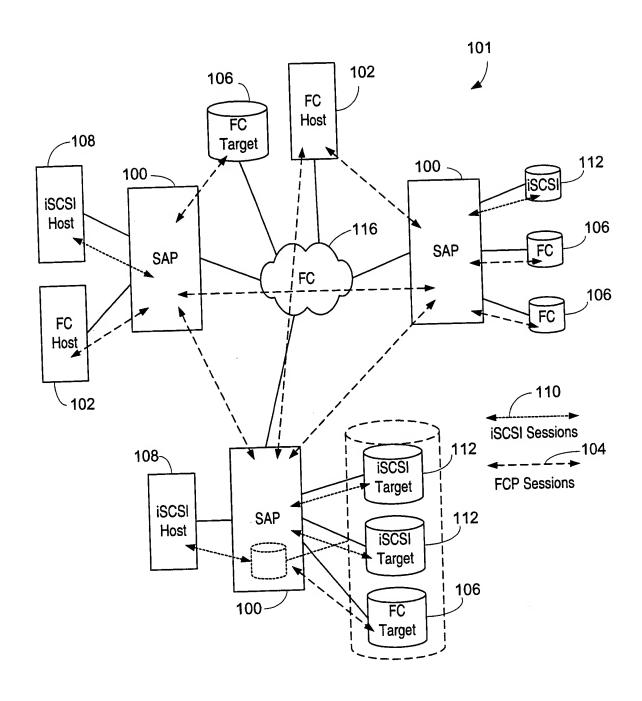


FIG. 1B

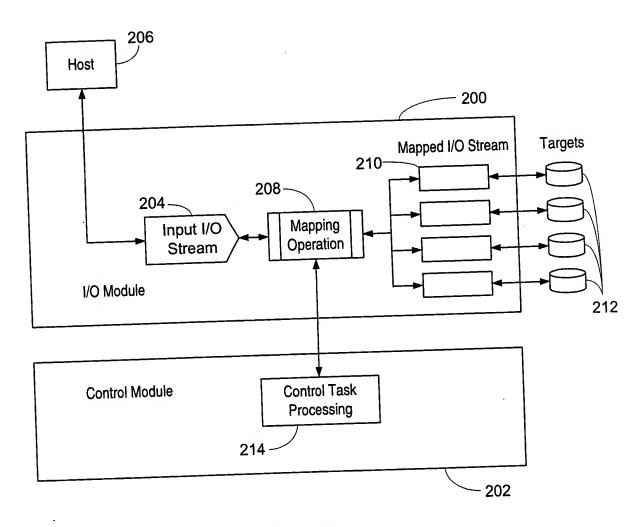


fig. 2

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Industry APIs Topology & Discovery Routines Network Management

Fibre Channel Connectivity
Processor
IP Connectivity Processor
Management Processor

Firmware

I/O Processor Port Processors

Custom Application Segments

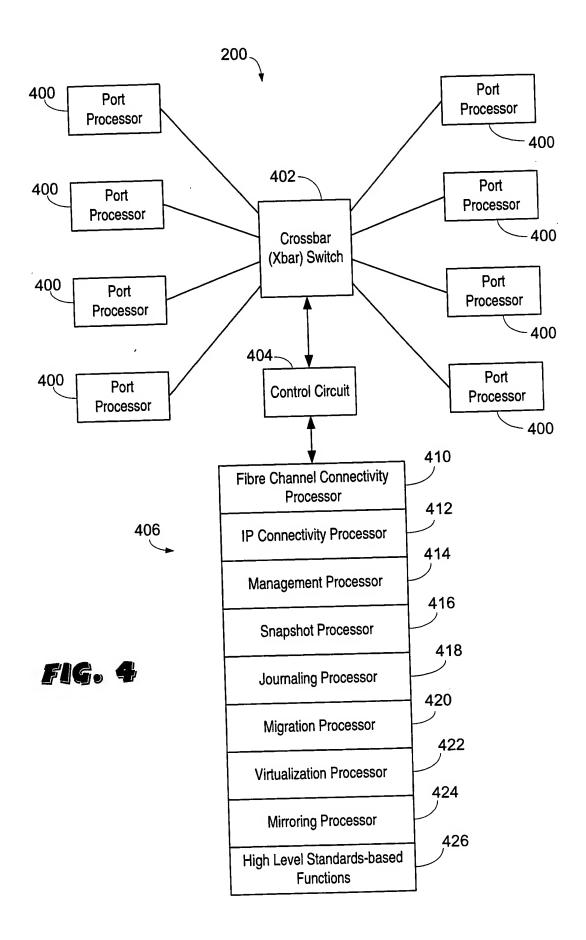
Virtualization Engine

Crossbar Switch

Silicon

Custom Application Segments





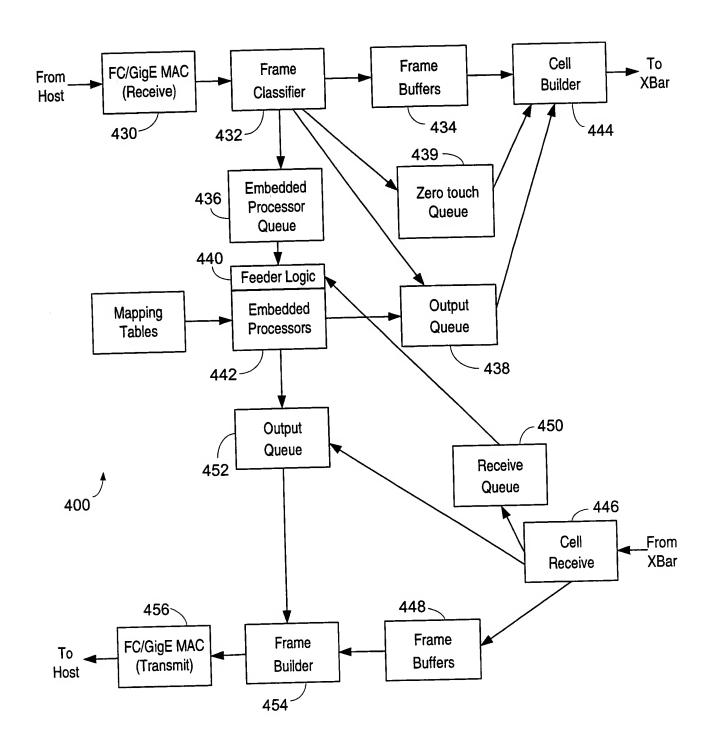


fig. 5

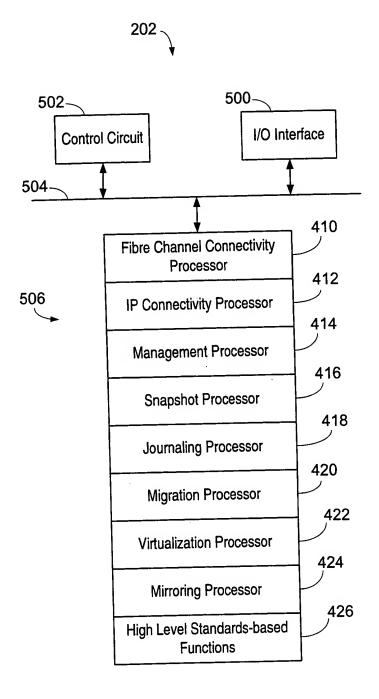


fig. 6

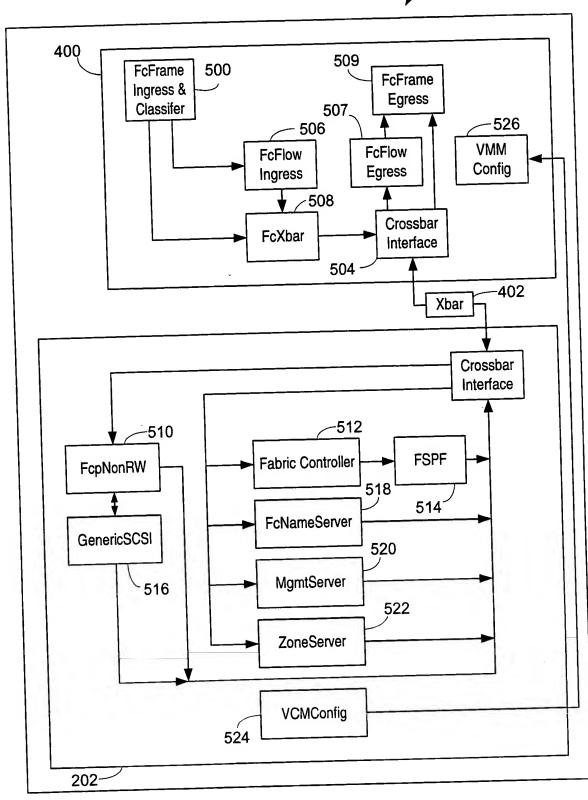


FIG. I

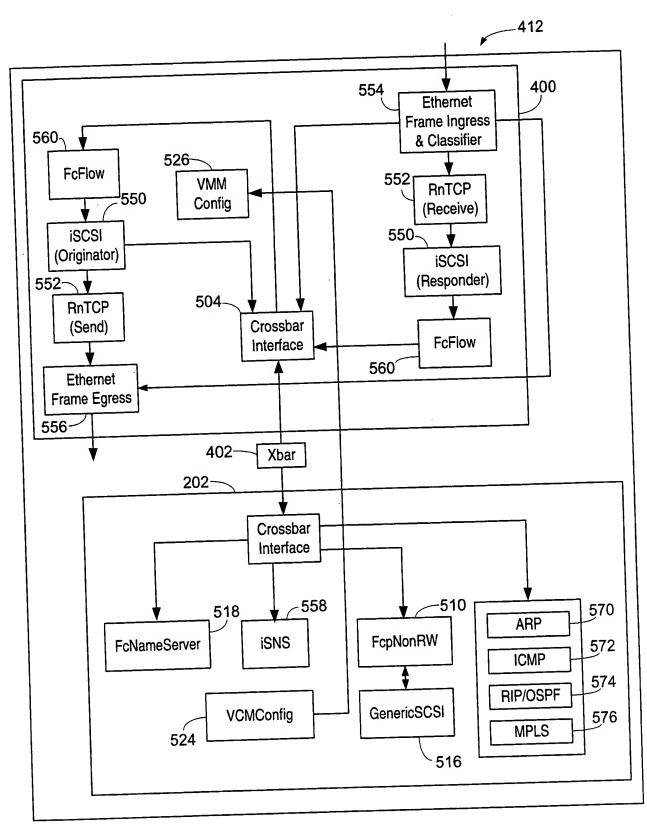
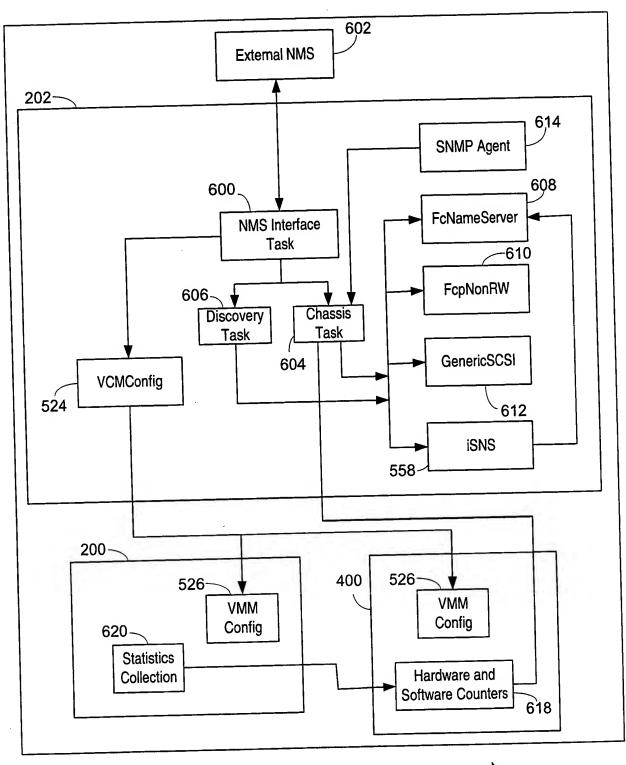


fig. 8



<del>-414</del>

fig. 9

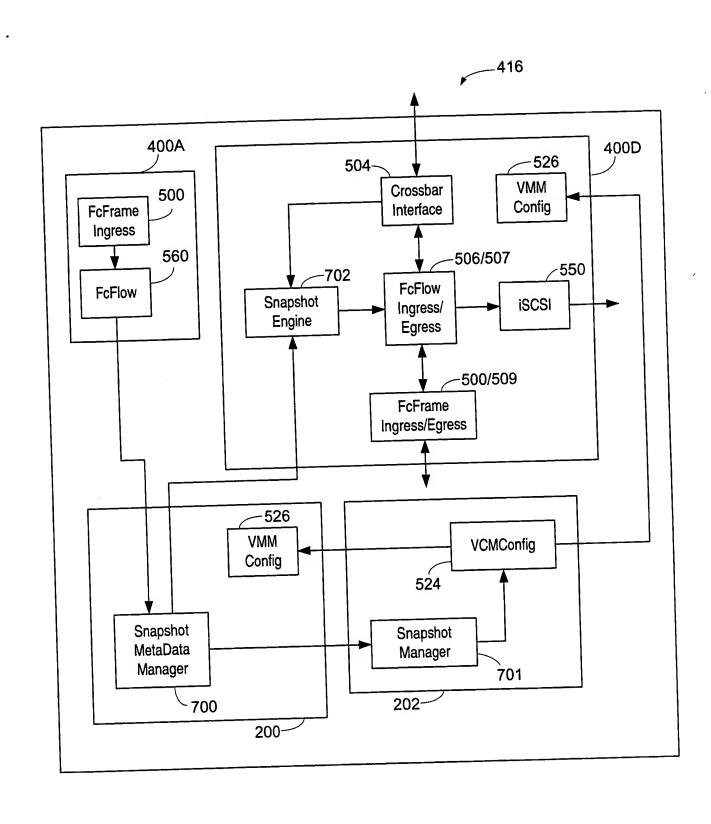


fig. 10

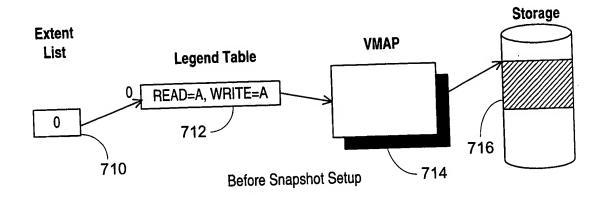


fig. 11

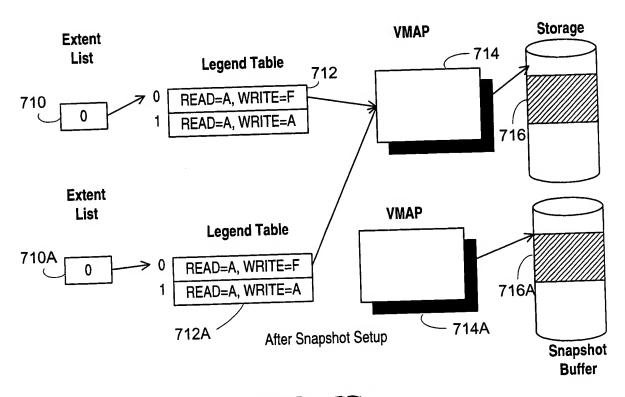


FIG. 12

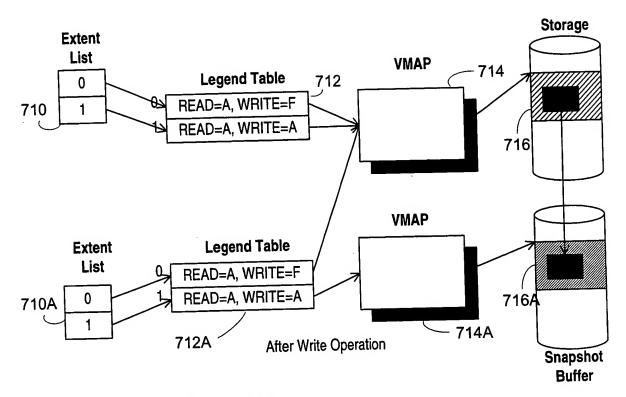
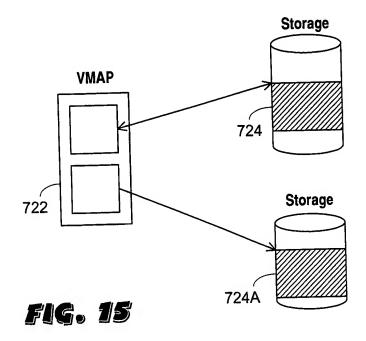
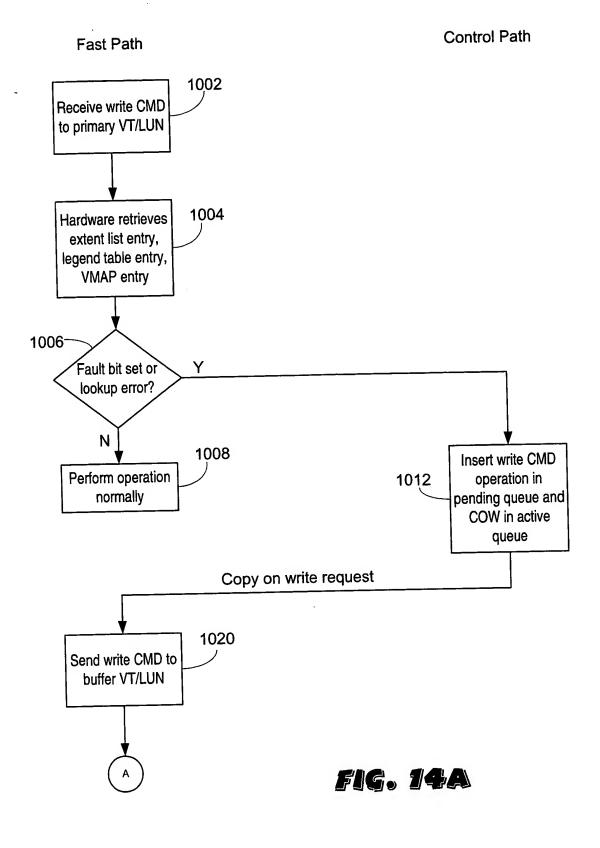
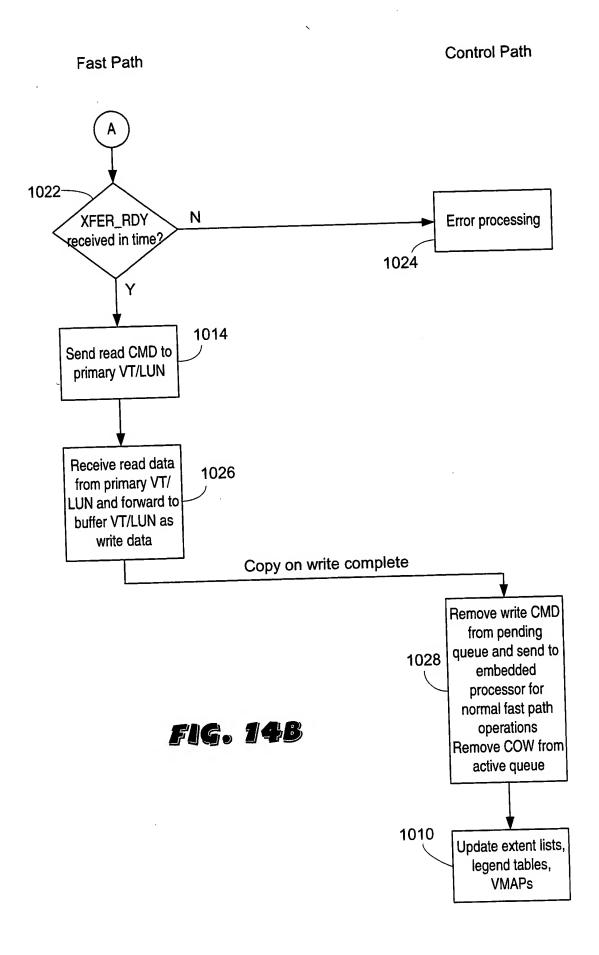


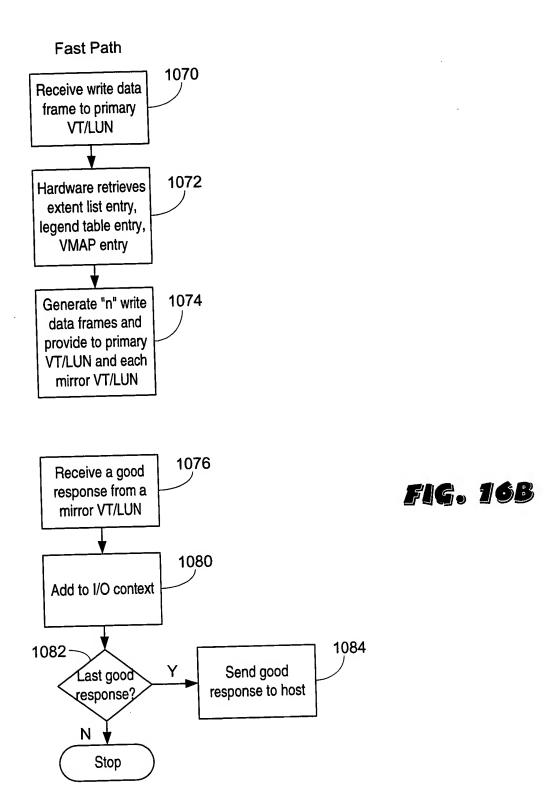
FIG. 13

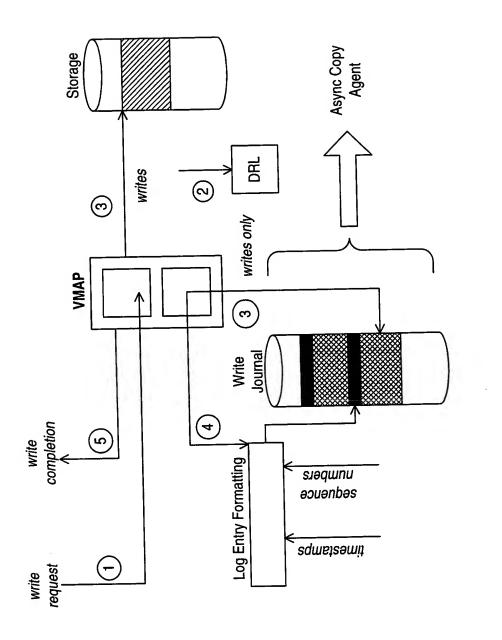




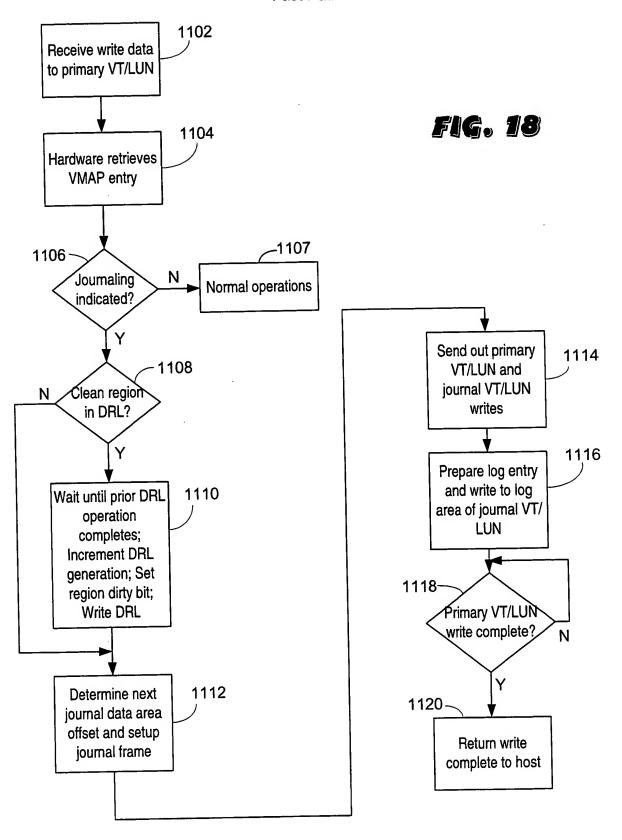


**Control Path** 





Fast Path



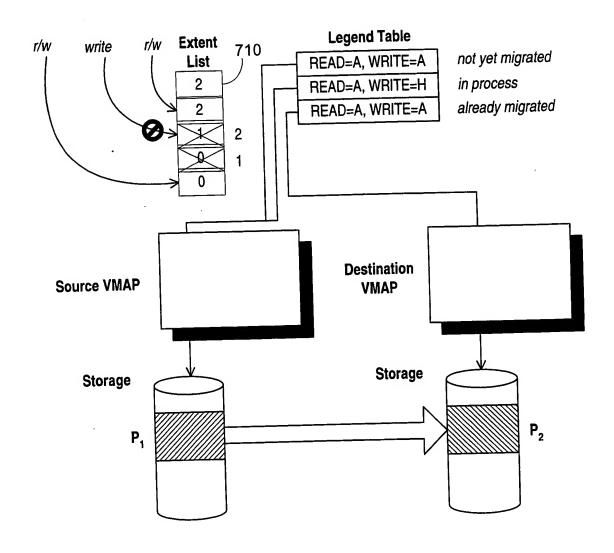
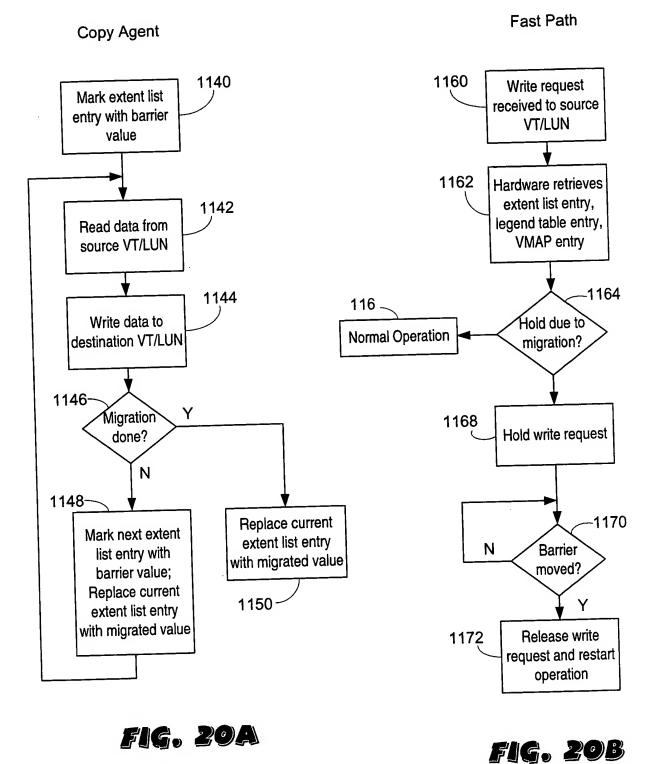
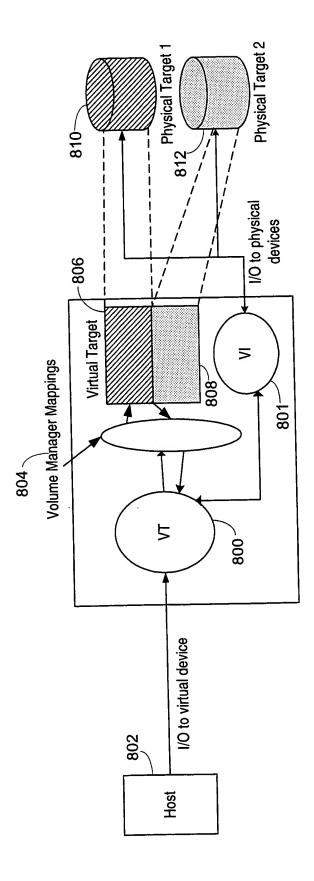
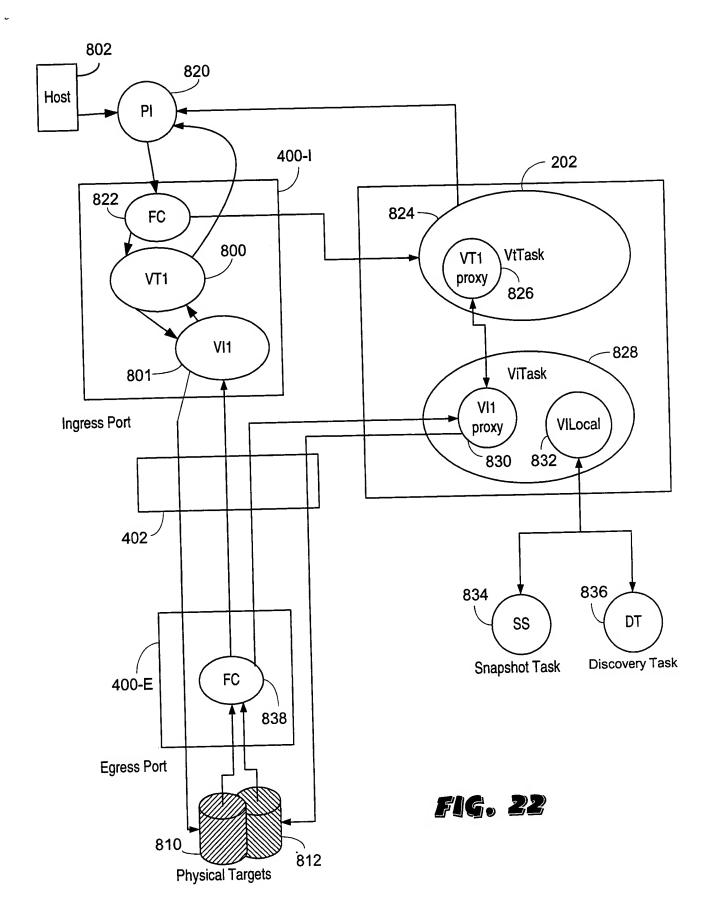


FIG. 19









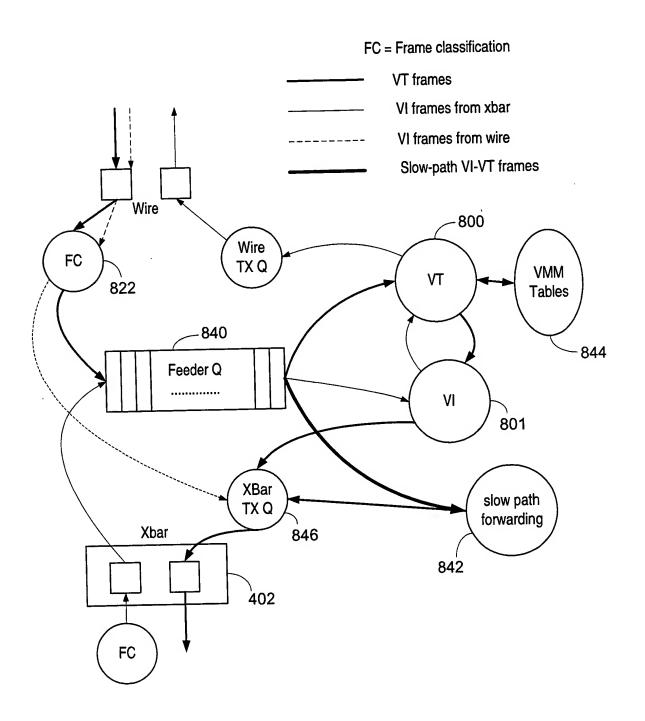
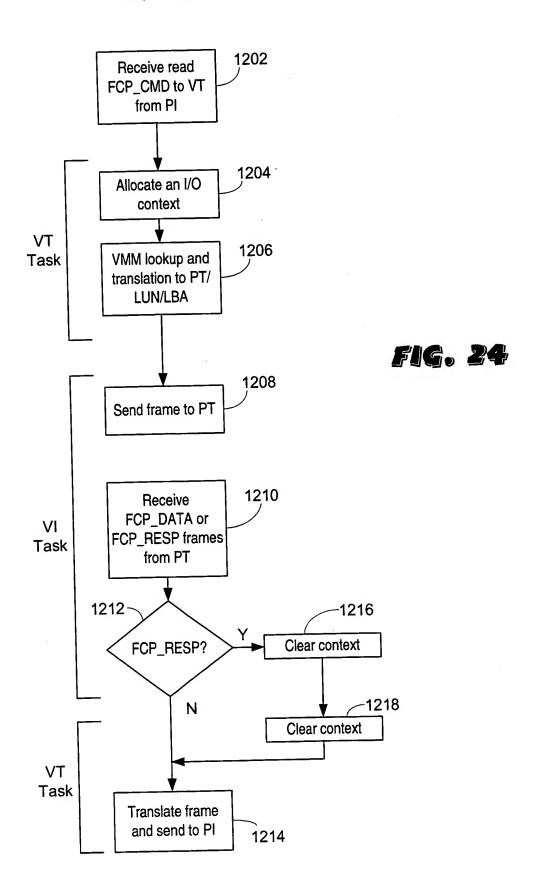


FIG. 23



## Simple Write

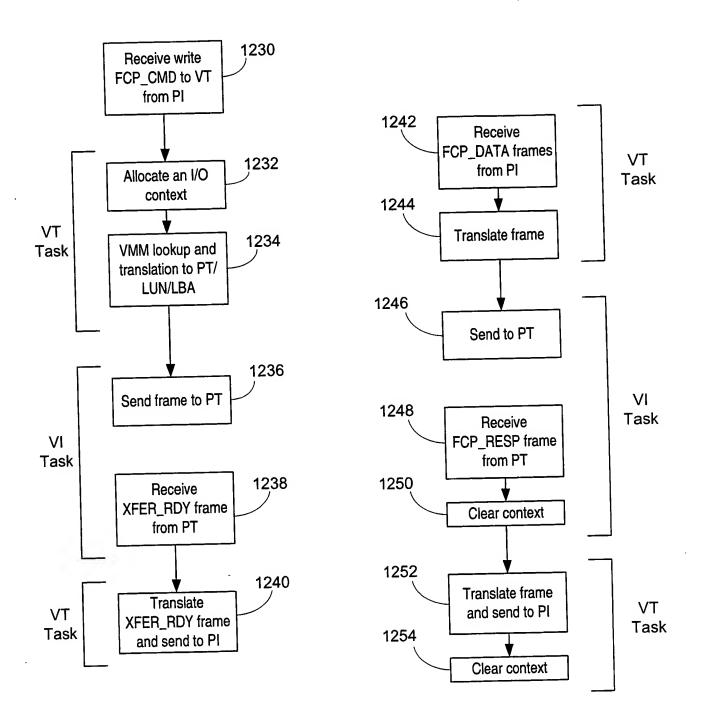


FIG. 25

## Spanned Read

